AMENDMENTS TO THE CLAIMS:

Without prejudice, this listing of the claims replaces all prior versions and listings of the claims in the present application:

LISTING OF CLAIMS:

1. (Currently Amended) A device for receiving data transmitted using asynchronous data transmission technology, in particular audio and video data, to which a data-independent clock signal is added, having a memory device (17), which stores the received data for the required period of time in order to compensate for transmission delays (Cell Delay Variation), characterized in that the clock signal is sent to the memory device (17) for readout of the data.

2. (Currently Amended) The device according to claim Claim 1, characterized in that it is designed for receiving data transmitted by the ATM (Asynchronous Data Transfer) technology.

3. (Currently Amended) The device according to claim Claim 1, characterized in that the memory device (17) is designed as a FIFO memory (19).

4. (Currently Amended) The device according to claim Claim 3, characterized in that the FIFO memory (19) is dimensioned so that the received data are storable for a period of preferably 100 μs to 250 μs per switching node.

5. (Currently Amended) The device according to claim 1, characterized in that a clock (21) providing a clock signal is synchronized with at least one other device (3; master/slave mode).

6. (Currently Amended) The device according to claim 1, characterized in that the clock (21) is not synchronized with the clock of the transmitting device, and means for adjusting the received data stream to the clock rate of the clock (21) are provided.

7. (Currently Amended) The device according to claim ~~Claim~~ 6, characterized in that the clock rate adjusting means double or omit certain data signals when reading from the memory device ~~(17)~~.

8. (Currently Amended) The device according to claim 1, characterized in that the clock ~~(21)~~ is synchronized via an external normal clock rate.

9. (Previously Presented) The device according to claim 1, characterized in that a switchover device is provided, which classifies the received data into data classes and relays them to a corresponding device.

10. (Currently Amended) A method of transmitting and receiving data signals, in particular audio and video data signals between two studios ~~(1, 3)~~, with each studio having a studio clock rate, characterized in that the data signals are transmitted using ATM technology, and the received data signals are temporarily stored and read out at the studio clock rate.

11. (Currently Amended) The method according to claim ~~Claim~~ 10, characterized in that the studio clock rates are synchronized.

12. (Currently Amended) The method according to claim ~~Claim~~ 10, characterized in that the received data signals are stored during a time period that is sufficient for compensating transmission delays.

13. (Previously Presented) The method according to claim 10, characterized in that in order to synchronize the studio clock rates, a normal clock rate of a network operator is supplied directly or indirectly.

14. (Previously Presented) The method according to claim 10, characterized in that the clock rate of one studio is used for synchronizing two studios (master/slave mode).

15. (Currently Amended) The method according to <u>claim</u> ~~Claim~~ 14, characterized in that a normal clock rate is sent to one studio, which is relayed to the other studio via the link line ~~(5)~~.

16. (Currently Amended) The method according to <u>claim</u> ~~Claim~~ 10, characterized in that, when the studio clock rates are not synchronized, the clock rates are adjusted using a plus/zero/minus packing procedure.

17. (Currently Amended) The method according to <u>claim</u> ~~Claim~~ 16, characterized in that when audio signals are transmitted, these are checked for irrelevant data signals and the data signals that are irrelevant for the clock rate adjustment are retransmitted in double or omitted.